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PATENT & TRADEMARK OFFICE

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PATENT
Docket No. INTEL/18500

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	I hereby certify that this paper and the documents referred to as enclosed therewith are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date:
Nagendra et al.	
U.S. Serial No.: 10/783,840	
For: "Methods and Apparatus to Optimize Managed Application Program Interfaces"	
Filed: February 20, 2004	**February 25, 2005**
Assignee: Intel Corporation	
Group Art Unit: 2122	Mark C. Zimmerman Attorney for Applicant(s) Registration No. 44,006
Examiner: Not Yet Assigned	

CROSS-REFERENCE UNDER 37 C.F.R. §1.78(a)(2) TO RELATED APPLICATIONS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §1.78(a)(2), applicants hereby cross-reference the following related application, which is assigned to the same assignee as the present application:

(1) U.S. Patent Application No. 11/019,803 filed on December 21, 2004, entitled: "USING OPTIMIZED LIBRARIES TO IMPROVE PERFORMANCE OF DEPLYED APPLICATION CODE AT RUNTIME" by Gururaj Nagendra and Stewart N. Taylor.

Respectfully submitted,

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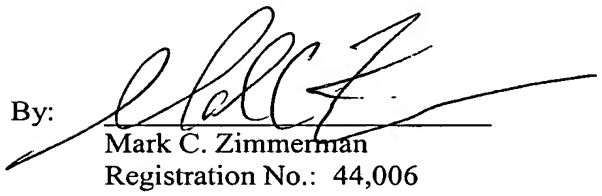
By: 

Mark C. Zimmerman
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February 25, 2005

Attorneys for Intel Corporation